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Chapter 1 Exercise Solutions. VLSI Test Principles and Architectures Ch. 1 - Introduction - P. 1/2. Chapter 1 Exercise Solutions. 1.1. There are 14 nodes in the circuit. Thus, there are $14 \times 2 = 28$ single stuck-at faults. For multiple stuck-at fault, it has $(2 + 1)14 - 1 = 4782968$ multiple stuck-at faults.

Chapter 1 Exercise Solutions
Si= Ci + Ai+ Bi, Ci+1= Ci(Ai + Bi) + Aibi. VLSI Test Principles and Architectures Ch. 2 - Design for Testability - P. 3/12. 2.5 (Ad Hoc Technique) Fig. 7: An example of a combinational feedback loop in a combinational circuit. Figure 7 shows an example of a combinational feedback loop. In this example, when A = 1, B = 0, and C = 1, the circuit acts as an oscillator, which prevents us from being able to predict a valid value at G.

Chapter 2 Exercise Solutions 2.1 Testability Analysis
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VLSI Test Principles and Architectures Ch. 6 - Test Compression - P. 1/3. Chapter 6 Exercise Solutions: 6.1. The complete dictionary at least includes the following five entries: 0000 0110 0100 0001 1100. The five 4-bit entries can be encoded into five 3-bit entries. The compression ratio is: $(1-3/4) \times 100\% = 25\%$. 6.2.

Chapter 6 Exercise Solutions - IC-Test Lab, NCUE, Taiwan
But chem(lat) = chem(vert), so. Solutions Manual SILICON VLSI TECHNOLOGY 140 © 2000 by Prentice Hall Fundamentals, Practice and Modeling Upper Saddle River, NJ. By Plummer, Deal and Griffin. $0.55 = \text{chem}(\text{vert}) / (\text{chem}(\text{vert}) + \text{phys}(\text{vert}))$ $0.55 = \text{chem}(\text{vert}) / \text{total}(\text{vert})$

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